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A diagram of a 7-bit shift register. It consists of seven rectangular cells connected in a horizontal line. Each cell has an input on the left and an output on the right. The inputs are labeled A, B, C, D, E, F, and G from left to right. The outputs are also labeled A, B, C, D, E, F, and G from left to right. The output of the first cell (A) is connected to the input of the second cell (B), and so on, forming a chain.

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2019-09-07

5270 Ref.5270 Perpetual Calendar Chronograph
Ref.5270 Cal.CH 29-535 PS Q
336541.0mm 18KWG

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2ch	1054
	4074
IWC	8811
Japan	8464
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